



Technical Document

Product Name : LED012-V1.0.0

Fiction Wang Jixin
Verify _____
Approve _____

Shanghai Enjie Electronic Technology Co., LTD



Record

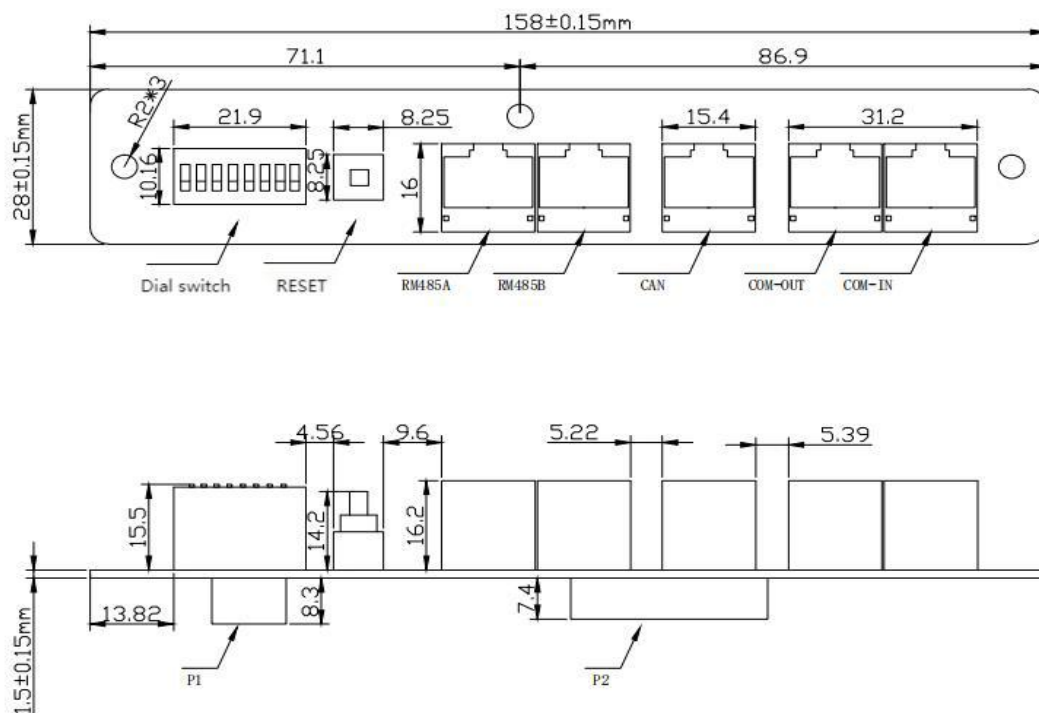
Document Number	Version	Modifier	Date	Main changes
LED012-V1.0.0-230302	V1.0	Wang Jixin	20230406	

一、Product Diagram

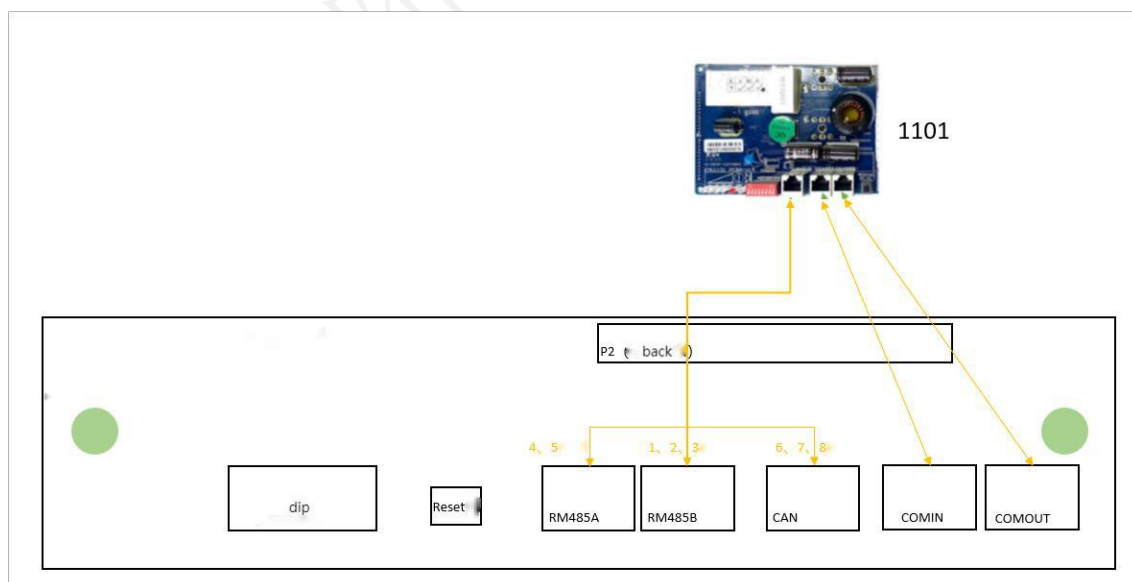
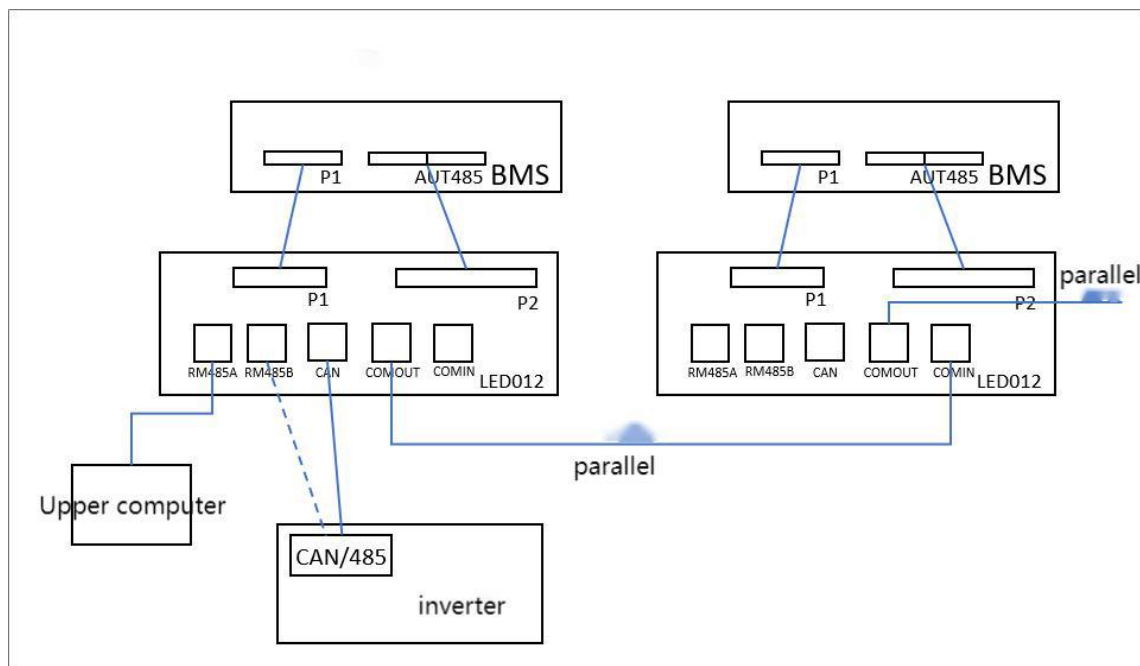
1、Picture of real products



2、Assumption diagram



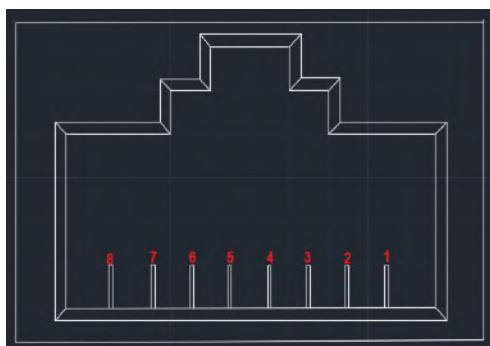
3、Connection diagram



二、Communication port pin definition

1、RM485-A、RM485-B

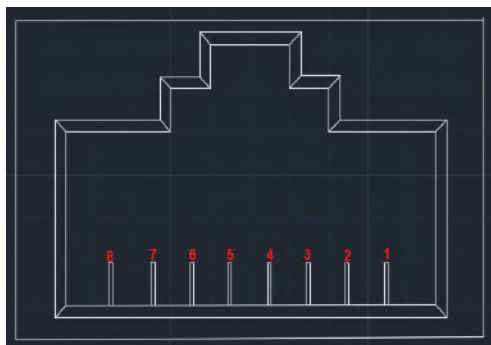
The communication interface with the inverter can realize 485 communication with the inverter according to the definition of RJ45 pins. It can also be connected to the upper computer (baud rate 9600).



Pin	Defined declaration
1、8	RS485-B
2、7	RS485-A
3、6	GND
4、5	vacancy

2、CAN

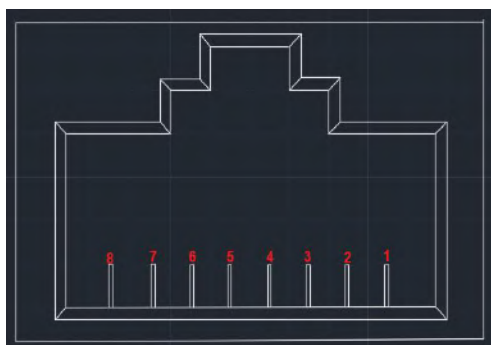
The communication interface with the inverter can realize CAN communication with the inverter according to the definition of RJ45 pins.



Pin	Defined declaration
1、 2、 7、 8	vacancy
3、 6	GND
4	CAN-H
5	CAN-L

3、COM-OUT

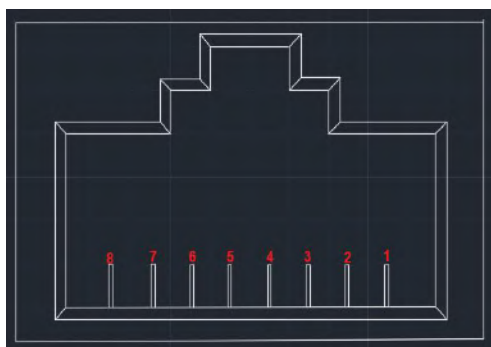
The PACK parallel communication interface can be connected to the COM-IN port of the next PACK to receive the information of the next PACK. It can also be connected to the upper computer (baud rate 19200).



Pin	Defined declaration
1、 8	RS485-B
2、 7	RS485-A
3、 5、 6	GND
4	Internal communications

4、COM-IN

The PACK parallel communication interface can be connected to the COM-OUT port of the previous PACK to upload the information of this group of PACK. It can also be connected to the upper computer (Baud rate 19200).



Pin	Defined declaration
1、 8	RS485-B
2、 7	RS485-A
3、 6	GND
4、 5	Internal communications